

CHAPTER 72—SEMICONDUCTOR RESEARCH

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SUBCHAPTER I—COOPERATIVE RESEARCH PROGRAM

§ 4601. Findings, purposes, and definitions

(a) Findings

The Congress finds that it is in the national economic and security interests of the United States for the Department of Defense to provide financial assistance to the industry consortium known as Sematech for research and development activities in the field of semiconductor manufacturing technology.

(b) Purposes

The purposes of this subchapter are—

(1) to encourage the semiconductor industry in the United States—

(A) to conduct research on advanced semiconductor manufacturing techniques; and

(B) to develop techniques to use manufacturing expertise for the manufacture of a variety of semiconductor products; and

(2) in order to achieve the purpose set out in paragraph (1), to provide a grant program for the financial support of semiconductor research activities conducted by Sematech.

(c) Definitions

In this subchapter:

(1) The terms “Semiconductor Technology Council” and “Council” mean the advisory council established by section 4603 of this title.

(2) The term “Sematech” means a consortium of firms in the United States semiconductor industry established for the purposes of (A) conducting research concerning advanced semiconductor manufacturing techniques, and (B) developing techniques to adapt manufacturing expertise to a variety of semiconductor products.

(Pub. L. 100–180, div. A, title II, § 271, Dec. 4, 1987, 101 Stat. 1068; Pub. L. 103–160, div. A, title II, § 263(c)(1), Nov. 30, 1993, 107 Stat. 1610.)

AMENDMENTS

1993—Subsec. (c)(1). Pub. L. 103–160 substituted “Semiconductor Technology Council” for “Advisory Council on Federal Participation in Sematech”.

§ 4602. Grants to Sematech

(a) Authority to make grants

The Secretary of Defense shall make grants, in accordance with section 6304 of title 31, to Sematech in order to defray expenses incurred by Sematech in conducting research on and development of semiconductor manufacturing technology. The grants shall be made in accordance with a memorandum of understanding entered into under subsection (b) of this section.

(b) Memorandum of understanding

The Secretary of Defense shall enter into a memorandum of understanding with Sematech for the purposes of this subchapter. The memorandum of understanding shall require the following:

(1) That Sematech have—

(A) a charter agreed to by all representatives of the semiconductor industry that are participating members of Sematech; and

(B) an annual operating plan that is developed in consultation with the Secretary of Defense and the Semiconductor Technology Council.

(2) That the total amount of funds made available to Sematech by Federal, State, and local government agencies for any fiscal year for the support of research and development activities of Sematech under this section may not exceed 50 percent of the total cost of such activities.

(3) That Sematech, in conducting research and development activities pursuant to the memorandum of understanding, cooperate with and draw on the expertise of the national laboratories of the Department of Energy and of colleges and universities in the United States in the field of semiconductor manufacturing technology.

(4) That an independent, commercial auditor be retained (A) to determine the extent to which the funds made available to Sematech by the United States for the research and development activities of Sematech have been expended in a manner that is consistent with the purposes of this subchapter, the charter of Sematech, and the annual operating plan of Sematech, and (B) to submit to the Secretary of Defense, Sematech, and the Comptroller General of the United States an annual report containing the findings and determinations of such auditor.

(5) That (A) the Secretary of Defense be permitted to use intellectual property, trade secrets, and technical data owned and developed by Sematech in the same manner as a participant in Sematech and to transfer such intellectual property, trade secrets, and technical data to Department of Defense contractors for use in connection with Department of Defense requirements, and (B) the Secretary not be

permitted to transfer such property to any person for commercial use.

(6) That Sematech take all steps necessary to maximize the expeditious and timely transfer of technology developed and owned by Sematech to the participants in Sematech in accordance with the agreement between Sematech and those participants and for the purpose of improving manufacturing productivity of United States semiconductor firms.

(c) Construction of memorandum of understanding

The memorandum of understanding entered into under subsection (b) of this section shall not be considered to be a contract for the purpose of any law or regulation relating to the formation, content, and administration of contracts awarded by the Federal Government and subcontracts under such contracts, including section 2306a of title 10, section 2168 of the Appendix to title 50, and the Federal Acquisition Regulations, and such provisions of law and regulation shall not apply with respect to the memorandum of understanding.

(d) Funding for FY88

Of the amounts appropriated to the Defense Agencies for fiscal year 1988 for research, development, test, and evaluation, \$100,000,000 may be obligated only to make grants under this section.

(Pub. L. 100-180, div. A, title II, § 272, Dec. 4, 1987, 101 Stat. 1068; Pub. L. 103-160, div. A, title II, § 263(c)(2), Nov. 30, 1993, 107 Stat. 1610.)

AMENDMENTS

1993—Subsec. (b)(1)(B). Pub. L. 103-160 substituted “Semiconductor Technology Council” for “Advisory Council on Federal Participation in Sematech”.

§ 4603. Semiconductor Technology Council

(a) Establishment

There is established the Semiconductor Technology Council.

(b) Purposes and functions

(1) The purposes of the Council are the following:

(A) To link assessment by the semiconductor industry of future market and national security needs to opportunities for technology development through cooperative public and private investment.

(B) To seek ways to respond to the technology challenges for semiconductors by fostering precompetitive cooperation among industry, the Federal Government, and institutions of higher education.

(C) To make available judgments, assessments, insights, and recommendations that relate to the opportunities for new research and development efforts and the potential to better rationalize and align industry and government contributions to semiconductor research and development.

(2) The Council shall carry out the following functions:

(A) Advise Sematech and the Secretary of Defense on appropriate technology goals and appropriate level of effort for the research and development activities of Sematech.

(B) Review the emerging markets, technology developments, and core technology challenges for semiconductor research and development and semiconductor manufacturing and explore opportunities for improved coordination among industry, the Federal Government, and institutions of higher education regarding such developments and challenges.

(C) Assess the effect on the appropriate role of Sematech of public and private sector international agreements in semiconductor research and development.

(D) Exchange views regarding the competitiveness of United States semiconductor technology and new or emerging semiconductor technologies that could affect national economic and security interests.

(E) Exchange and update information and identify overlaps and gaps regarding the efforts of industry, the Federal Government, and institutions of higher education in semiconductor research and development.

(F) Assess technology progress relative to industry requirements and Federal Government requirements, responding as appropriate to the challenges in the national semiconductor technology roadmap developed by representatives of industry, the Federal Government, and institutions of higher education.

(G) Make recommendations regarding the semiconductor technology development efforts that should be supported by Federal agencies and industry.

(H) Appoint subgroups as appropriate in connection with the updating of the semiconductor technology roadmap.

(I) Publish and submit to Congress by March 31 of each year an annual report addressing the semiconductor technology challenges and developments for industry, government, and institutions of higher education and the relationship among the challenges and developments for each, including an evaluation of the role of Sematech.

(c) Membership

The Council shall be composed of 16 members as follows:

(1) The Under Secretary of Defense for Acquisition, Technology, and Logistics who shall be Cochairman of the Council.

(2) The Under Secretary of Energy responsible for science and technology matters.

(3) The Under Secretary of Commerce for Technology.

(4) The Director of the Office of Science and Technology Policy.

(5) The Assistant to the President for Economic Policy.

(6) The Director of the National Science Foundation.

(7) Ten members appointed by the President as follows:

(A) Four individuals who are eminent in the semiconductor device industry, one of whom shall be Cochairman of the Council.

(B) Two individuals who are eminent in the semiconductor equipment and materials industry.

(C) Three individuals who are eminent in the semiconductor user industry, including